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Since the realization of silicon thin film transfer by wafer bonding and hydrogen ion implantation and exfoliation (Smart Cut<sup>®</sup>) by Bruel in 1995<sup>1</sup>, several teams have succeeded in transferring a variety of semiconductor films including GaAs<sup>2</sup>, InP<sup>3,4</sup>, SiC<sup>5,6,7</sup>, Ge<sup>8</sup>, GaSb<sup>9,10</sup>, SrTiO<sub>3</sub><sup>11</sup>, LaAlO<sub>3</sub> and sapphire<sup>12</sup>. In addition to using hydrogen ion implantation, He<sup>13</sup>, He and H<sup>13,14</sup>, H and B<sup>15,16</sup> and Si and H<sup>17</sup> co-implantations have been successfully used to realize either reduced total dose and/or temperature required for exfoliation of Si. To transfer useful films, a stiffer substrate must be bonded to the implanted substrate as in, for example, the UNIBOND SOI fabrication process<sup>18</sup>. Suitable wafer bonding techniques must provide the required bond strength in order to direct the internal forces of exfoliation parallel to the bond interface allowing the transfer of a continuous semiconductor film. The exfoliation temperature may be relatively low, and accordingly, low-temperature wafer bonding techniques are required. For example, exfoliation and layer transfer have been performed at temperatures as low as 300°C in Si co-implanted with H and B<sup>15</sup>. More recently, room temperature cleaving has been achieved by mechanical separation of the film and substrate<sup>19,20</sup>. These processes require very high bond strength at room temperature which have been achieved thus far by plasma preparation of the bonding surfaces.

This paper will review recent progress in thin film transfer using implantation and exfoliation. Key applications will be reviewed and progress cited.

### References:

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